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<p>Top Overlay (Scale 1:1)</p> <p>100.00mm</p> <p>92.75mm</p>	<p>Bill Of Materials</p> <table border="1"> <thead> <tr> <th>Line #</th><th>Designator</th><th>Comment</th><th>Volt</th><th>Capacitor Type</th><th>Tolleranc e</th><th>Spec</th><th>Footprint</th><th>Quantity</th></tr> </thead> <tbody> <tr><td>1</td><td>C1, C3, C7, C8, C11, C12, C16, C17, C18, C19, C22, C23, C24, C25, C26, C27, C28, C29</td><td>100nF</td><td>50V</td><td></td><td></td><td>PPS, X7R</td><td>CAP_1913, CAP_0805</td><td>18</td></tr> <tr><td>2</td><td>C2, C4</td><td>220pF</td><td>50V</td><td></td><td></td><td>PPS</td><td>CAP_0805</td><td>2</td></tr> <tr><td>3</td><td>C5, C9, C10, C13, C14, C15, C21</td><td>10uF</td><td>20V</td><td>Tant.</td><td></td><td></td><td>CAP_POL_1206</td><td>7</td></tr> <tr><td>4</td><td>C6, C20</td><td>100uF</td><td>25V</td><td>Elect.</td><td></td><td></td><td>CAP_SMD_6</td><td>2</td></tr> <tr><td>5</td><td>C30, C31</td><td>10nF</td><td>50V</td><td></td><td></td><td>X7R</td><td>CAP_0805</td><td>2</td></tr> <tr><td>6</td><td>D1, D2, D4, D6, D8, D9, D10</td><td>1N4148W</td><td></td><td></td><td></td><td></td><td>SOD-123</td><td>7</td></tr> <tr><td>7</td><td>D3, D5, D7</td><td>DZ2W04300L</td><td>4V3</td><td></td><td></td><td></td><td>SOD-123</td><td>3</td></tr> <tr><td>8</td><td>E1</td><td>EC11B15242AF</td><td></td><td></td><td></td><td></td><td>CON_5_ENC</td><td>1</td></tr> <tr><td>9</td><td>F1, F2, F3, F4, F5, F6, F7, F8, F9, F10</td><td>MPZ2012S331A</td><td></td><td></td><td></td><td></td><td>FE_0805</td><td>10</td></tr> <tr><td>10</td><td>J1</td><td>Jack 6.35mm</td><td></td><td></td><td></td><td></td><td>CL12327</td><td>1</td></tr> <tr><td>11</td><td>J2</td><td>IN_B</td><td></td><td></td><td></td><td></td><td>CON_3</td><td>1</td></tr> <tr><td>12</td><td>J3</td><td>IN_A</td><td></td><td></td><td></td><td></td><td>CON_3</td><td>1</td></tr> <tr><td>13</td><td>J4</td><td>PSU_A_EXT</td><td></td><td></td><td></td><td></td><td>CON_3</td><td>1</td></tr> <tr><td>14</td><td>J5</td><td>PSU_A_M</td><td></td><td></td><td></td><td></td><td>CON_4x2_M</td><td>1</td></tr> <tr><td>15</td><td>J6</td><td>PSU_D_M</td><td></td><td></td><td></td><td></td><td>CON_4x2_M</td><td>1</td></tr> <tr><td>16</td><td>J7</td><td>Program</td><td></td><td></td><td></td><td></td><td>Con_6_PicKit_III</td><td>1</td></tr> <tr><td>17</td><td>J8</td><td>DIG_SEL</td><td></td><td></td><td></td><td></td><td>CON_5</td><td>1</td></tr> <tr><td>18</td><td>J9</td><td>DIG_SEL_2</td><td></td><td></td><td></td><td></td><td>CON_5</td><td>1</td></tr> <tr><td>19</td><td>K1</td><td>TX2SA</td><td></td><td></td><td></td><td></td><td>TX2SA</td><td>1</td></tr> <tr><td>20</td><td>M3 1, M3 2, M3 3, M3 4</td><td>Screw</td><td></td><td></td><td></td><td></td><td>SCREW M3</td><td>4</td></tr> <tr><td>21</td><td>OLED1</td><td>OLED SSD1306</td><td></td><td></td><td></td><td></td><td>OLED_SSD1306_V_1.3</td><td>1</td></tr> <tr><td>22</td><td>Q1</td><td>BC817</td><td></td><td></td><td></td><td></td><td>SOT23</td><td>1</td></tr> <tr><td>23</td><td>R1, R8, R10, R15</td><td>49.9R</td><td></td><td>1%</td><td></td><td></td><td>RES_0805</td><td>4</td></tr> <tr><td>24</td><td>R2, R3, R6, R7</td><td>1K</td><td></td><td>1%</td><td></td><td></td><td>RES_0805</td><td>4</td></tr> <tr><td>25</td><td>R4, R16</td><td>8K2</td><td></td><td>1%</td><td></td><td></td><td>RES_0805</td><td>2</td></tr> <tr><td>26</td><td>R5, R17</td><td>820K</td><td></td><td>1%</td><td></td><td></td><td>RES_0805</td><td>2</td></tr> <tr><td>27</td><td>R9, R11</td><td>10R</td><td></td><td>1%</td><td></td><td></td><td>RES_1206</td><td>2</td></tr> <tr><td>28</td><td>R12, R24, R25, R26, R27, R28, R29</td><td>10K</td><td></td><td>5%</td><td></td><td></td><td>RES_0805</td><td>7</td></tr> <tr><td>29</td><td>R13, R14</td><td>4.02K</td><td></td><td>1%</td><td></td><td></td><td>RES_0805</td><td>2</td></tr> <tr><td>30</td><td>R18</td><td>4K7</td><td></td><td>5%</td><td></td><td></td><td>RES_0805</td><td>1</td></tr> <tr><td>31</td><td>R19</td><td>33K</td><td></td><td>5%</td><td></td><td></td><td>RES_0805</td><td>1</td></tr> <tr><td>32</td><td>R20</td><td>0R</td><td></td><td></td><td></td><td></td><td>RES_1206</td><td>1</td></tr> <tr><td>33</td><td>R21</td><td>1K2</td><td></td><td>5%</td><td></td><td></td><td>RES_0805</td><td>1</td></tr> <tr><td>34</td><td>R22, R23</td><td>3K3</td><td></td><td>5%</td><td></td><td></td><td>RES_0805</td><td>2</td></tr> <tr><td>35</td><td>R30</td><td>5K6</td><td></td><td>5%</td><td></td><td></td><td>RES_0805</td><td>1</td></tr> <tr><td>36</td><td>R31, R32</td><td>2K2</td><td></td><td>5%</td><td></td><td></td><td>RES_0805</td><td>2</td></tr> <tr><td>37</td><td>R33</td><td>100K</td><td></td><td>5%</td><td></td><td></td><td>RES_0805</td><td>1</td></tr> <tr><td>38</td><td>S1</td><td>SW-PB</td><td></td><td></td><td></td><td></td><td>TACTIONSWITCHES 90</td><td>1</td></tr> <tr><td>39</td><td>TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8, TP9</td><td>=name</td><td></td><td></td><td></td><td></td><td>TEST POINT</td><td>9</td></tr> <tr><td>40</td><td>U1</td><td>OPA1612</td><td></td><td></td><td></td><td></td><td>SOIC8</td><td>1</td></tr> <tr><td>41</td><td>U2</td><td>PGA2320</td><td></td><td></td><td></td><td></td><td>SOIC16 - DW</td><td>1</td></tr> <tr><td>42</td><td>U3</td><td>TPA6120A2DWP</td><td></td><td></td><td></td><td></td><td>HSOP20</td><td>1</td></tr> <tr><td>43</td><td>U4, U5</td><td>OPA2134</td><td></td><td></td><td></td><td></td><td>SOIC8</td><td>2</td></tr> <tr><td>44</td><td>U6</td><td>PIC18F26K22-I/Sx</td><td></td><td></td><td></td><td></td><td>SOIC28</td><td>1</td></tr> <tr><td>45</td><td>U7</td><td>TSOP34836</td><td></td><td></td><td></td><td></td><td>TSOP348</td><td>1</td></tr> </tbody> </table>	Line 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CAP_POL_1206	7	4	C6, C20	100uF	25V	Elect.			CAP_SMD_6	2	5	C30, C31	10nF	50V			X7R	CAP_0805	2	6	D1, D2, D4, D6, D8, D9, D10	1N4148W					SOD-123	7	7	D3, D5, D7	DZ2W04300L	4V3				SOD-123	3	8	E1	EC11B15242AF					CON_5_ENC	1	9	F1, F2, F3, F4, F5, F6, F7, F8, F9, F10	MPZ2012S331A					FE_0805	10	10	J1	Jack 6.35mm					CL12327	1	11	J2	IN_B					CON_3	1	12	J3	IN_A					CON_3	1	13	J4	PSU_A_EXT					CON_3	1	14	J5	PSU_A_M					CON_4x2_M	1	15	J6	PSU_D_M					CON_4x2_M	1	16	J7	Program					Con_6_PicKit_III	1	17	J8	DIG_SEL					CON_5	1	18	J9	DIG_SEL_2					CON_5	1	19	K1	TX2SA					TX2SA	1	20	M3 1, M3 2, M3 3, M3 4	Screw					SCREW M3	4	21	OLED1	OLED SSD1306					OLED_SSD1306_V_1.3	1	22	Q1	BC817					SOT23	1	23	R1, R8, R10, R15	49.9R		1%			RES_0805	4	24	R2, R3, R6, R7	1K		1%			RES_0805	4	25	R4, R16	8K2		1%			RES_0805	2	26	R5, R17	820K		1%			RES_0805	2	27	R9, R11	10R		1%			RES_1206	2	28	R12, R24, R25, R26, R27, R28, R29	10K		5%			RES_0805	7	29	R13, R14	4.02K		1%			RES_0805	2	30	R18	4K7		5%			RES_0805	1	31	R19	33K		5%			RES_0805	1	32	R20	0R					RES_1206	1	33	R21	1K2		5%			RES_0805	1	34	R22, R23	3K3		5%			RES_0805	2	35	R30	5K6		5%			RES_0805	1	36	R31, R32	2K2		5%			RES_0805	2	37	R33	100K		5%			RES_0805	1	38	S1	SW-PB					TACTIONSWITCHES 90	1	39	TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8, TP9	=name					TEST POINT	9	40	U1	OPA1612					SOIC8	1	41	U2	PGA2320					SOIC16 - DW	1	42	U3	TPA6120A2DWP					HSOP20	1	43	U4, U5	OPA2134					SOIC8	2	44	U6	PIC18F26K22-I/Sx					SOIC28	1	45	U7	TSOP34836					TSOP348	1	<p>Drill Drawing View (Scale 1:1)</p>	<p>Drill Table</p> <table border="1"> <thead> <tr> <th>Symbol</th><th>Count</th><th>Hole Size</th><th>Plated</th><th>Hole Tolerance</th></tr> </thead> <tbody> <tr><td>◊</td><td>48</td><td>0.30</td><td>Plated</td><td>None</td></tr> <tr><td>◇</td><td>9</td><td>0.50</td><td>Plated</td><td>None</td></tr> <tr><td>✳</td><td>38</td><td>0.70</td><td>Plated</td><td>None</td></tr> <tr><td>✚</td><td>3</td><td>0.80</td><td>Plated</td><td>None</td></tr> <tr><td>◇</td><td>55</td><td>0.90</td><td>Plated</td><td>None</td></tr> <tr><td>✖</td><td>2</td><td>1.10</td><td>Plated</td><td>None</td></tr> <tr><td>○</td><td>6</td><td>1.70</td><td>Plated</td><td>None</td></tr> <tr><td>☆</td><td>2</td><td>2.00</td><td>Non-Plated</td><td>None</td></tr> <tr><td>□</td><td>4</td><td>3.00</td><td>Non-Plated</td><td>None</td></tr> <tr><td colspan="5">167 Total</td></tr> </tbody> </table>	Symbol	Count	Hole Size	Plated	Hole Tolerance	◊	48	0.30	Plated	None	◇	9	0.50	Plated	None	✳	38	0.70	Plated	None	✚	3	0.80	Plated	None	◇	55	0.90	Plated	None	✖	2	1.10	Plated	None	○	6	1.70	Plated	None	☆	2	2.00	Non-Plated	None	□	4	3.00	Non-Plated	None	167 Total					<p>1</p> <p>2</p> <p>3</p> <p>4</p>	<p>1</p> <p>2</p> <p>3</p> <p>4</p>
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